



SRI VASAVI ENGINEERING COLLEGE (Autonomous)

Pedatadepalli,TADEPALLIGUDEM-534101,W.G.Dist.

Department of Electronics and Communication Engineering



Ref.No: SVEC/ECE/Circular/2025-26/01

CLASS CONSOLIDATED TIME TABLE

Class: III Semester

Section: A

Class Coordinator: Sri.A.SURESH

w.e.f.24-07-2025

Room No.: D-302

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	PTSP	SS	ES	SS	Lunch Break	STLD	EDC	Sports
Tue	EDC Lab					STLD	PTSP	EDC
Wed	UHV	EDC	ES	LIB		PTSP	SS	STLD
Thu	PTSP	PCS-1		STLD		SS	EDC	Counseling
Fri	EDC	SS	UHV	PTSP		STLD Lab		
Sat	STLD	EDC	SS	UHV		STLD	PTSP	Sports

Section: B

Class Coordinator: Sri.S.N.DATTATREYACHARI

Room No.: D-303

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	EDC	PTSP	ES	LIB	Lunch Break	UHV	SS	STLD
Tue	UHV	PTSP	STLD	SS		EDC	ES	Counseling
Wed	STLD Lab					STLD	EDC	PTSP
Thu	SS	EDC	PTSP	EDC		PTSP	STLD	Sports
Fri	STLD	PCS-1		SS		UHV	SS	Sports
Sat	PTSP	SS	STLD	EDC		EDC Lab		

Section: C

Class Coordinator: Smt. QURRATUL AYN

Room No.: D-304

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	STLD	SS	EDC	SS	Lunch Break	EDC Lab		
Tue	EDC	PTSP	ES	Counseling		UHV	SS	EDC
Wed	PTSP	SS	STLD	UHV		EDC	PTSP	Sports
Thu	ES	STLD	EDC	PTSP		STLD	PTSP	LIB
Fri	SS	PCS-1		PTSP		SS	EDC	STLD
Sat	STLD Lab					UHV	STLD	Sports

Section: D

Class Coordinator: Sri. K. PASIPALANA RAO

Room No.: D-305

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	PTSP	ES	STLD	LIB	Lunch Break	EDC	SS	UHV
Tue	SS	STLD	EDC	PTSP		STLD Lab		
Wed	STLD	ES	PTSP	SS		PCS-1		STLD
Thu	SS	PTSP	UHV	Counseling		EDC	EDC	Sports
Fri	EDC Lab					STLD	PTSP	SS
Sat	UHV	EDC	SS	STLD		PTSP	EDC	Sports

Staff Details:

S. No.	Sub	Course Code	Course Name	SECTION			
				A	B	C	
1.	PTSP	V23ECT02	Probability Theory And Stochastic Process	Sri P.SRINIVAS	Sri P.SRINIVAS	Smt. J. DHANASREE	Smt. J.DHANASREE
2.	SS	V23ECT03	Signals And Systems	Sri G. SHANKARA BHASKARA RAO	Sri G. SHANKARA BHASKARA RAO	Sri D.R.SANDEEP	Sri D.R.SANDEEP
3.	EDC	V23ECT04	Electronic Devices And Circuits	Sri. ANGADI SURESH	Sri. ANGADI SURESH	Sri D.VENKANNA BABU	Sri D.VENKANNA BABU
4.	STLD	V23ECT05	Switching Theory And Logic Design	Sri DATTATREYA	Sri DATTATREYA	Sri K. PASIPALANA RAO	Sri K. PASIPALANA RAO
5.	UHV	V23MBT53	Universal Human Values	Sri K.SAI SUDHEER	Sri M.PREMCHAND	Dr. M. THAMARAI	Sri R.L.R LOKESH BABU
6.	ES	V23CEAC01	Environmental Science	Mrs. L. JYOTSNA	Ms.E. RAMYA SATYA SRI	Mr. J. N .B .V .CHANDRA SEKHAR	Ms.E. RAMYA SATYA SRI
7.	EDC LAB	V23ECL02	Electronic Devices And Circuits Lab	Sri. A. SURESH Dr.T.V.N.L. ASWINI Sri T.SREENIVASU Dr.CH. V NAGA BHASKAR	Sri J.RAJENDRA Sri V. ANIL KUMAR Sri T.SREENIVASU Sri N.SAIDULU	Sri V. ANIL KUMAR Dr.CH. V NAGA BHASKAR Sri R. RAM PRASAD Sri.J.RAJENDRA	Smt.Q. AYN Sri N.SAIDULU Sri T.SREENIVASU Dr. KNH. SRINIVAS
8.	STLD LAB	V23ECL03	Switching Theory And Logic Design Lab	Sri P.SRINIVAS Sri R.L.R.LOKESH BABU Sri DATTATREYA Sri M.PITCHAIAH	Sri P.SRINIVAS Sri DATTATREYA Sri R.L.R.LOKESH BABU Sri V.ANIL KUMAR	Smt. J. DHANASREE Dr. S.V.V. SATYANARAYANA Sri P.SRINIVAS Sri D.VENKANNA BABU	Smt. J. DHANASREE Sri K. PASIPALANA RAO Sri D.VENKANNA BABU Sri M. PITCHAIAH
9.	PCS-I		Professional Communication Skills-I	Ms.CH.TANUJA Mr.M.VENKATA RAMANA	Mr.M.VENKATA RAMANA Dr.K.VENKATA RAO	MR.G.SRINIVASA RAO Dr.B.ANANDA RAO	Dr.K.VENKATA RAO MS.A.KIRANMAYEE



Head of the Department

Vision

- To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and communication engineers

Mission

- To create a learner centric environment that promotes the intellectual growth of the students.
- To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
- To build the student community with high ethical standards.



SRI VASAVI ENGINEERING COLLEGE (Autonomous)

Pedatadepalli,TADEPALLIGUDEM-534101,W.G.Dist.

Department of Electronics and Communication Technology



Ref. No: SVEC/ECE/Circular/2025-26/01

CLASS CONSOLIDATED TIME TABLE

Class: III Semester

Section: ECT

Class Coordinator: **Sri.J.RAJENDRA**

w.e.f.24-07-2025

Room No.: A-204

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	STLD Lab				Lunch Break	STLD	UHV	EDC
Tue	PTSP	ES	STLD	LIB		EDC	UHV	SS
Wed	PTSP	SS	PTSP	UHV		STLD	PTSP	EDC
Thu	EDC	EDC Lab				SS	PTSP	Sports
Fri	STLD	EDC	STLD	SS		PCS-1		Counseling
Sat	SS	STLD	PTSP	EDC		ES	SS	Sports

Staff Details:

S. NO.	Course Code	SUB	Course Name	SECTION
				ECT
1.	V23ECT02	PTSP	Probability Theory And Stochastic Process	Sri J. RAJENDRA
2.	V23ECT03	SS	Signals And Systems	Dr.CH. V NAGA BHASKAR
3.	V23ECT04	EDC	Electronic Devices And Circuits	Sri D.VENKANNA BABU
4.	V23ECT05	STLD	Switching Theory And Logic Design	Sri K. PASIPALANA RAO
5.	V23MBT53	UHV	Universal Human Values	Smt. Y.SUJATHA
6.	V23CEAC01	ES	Environmental Science	Smt. Mohammad Sayyad Yasmin
7.	V23ECL02	EDC LAB	Electronic Devices And Circuits Lab	Sri N.SAIDULU Sri G.SHANKARA BHASKARA RAO Dr.K.N.H.SRINIVAS Dr.CH. V NAGA BHASKAR
8.	V23ECL03	STLD LAB	Switching Theory And Logic Design Lab	Sri J. RAJENDRA Smt. J. DHANASREE Sri DATTATREYA Dr. T.D.N.S.S. SARVESWARA RAO
9.		PCS-I	Professional Communication Skills-I	Dr.K.EVINKATA RAO Mrs.Ch.TANUJA

E. Kumar
Head of the Department

Vision

- To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and communication engineers

Mission

- To create a learner centric environment that promotes the intellectual growth of the students.
- To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
- To build the student community with high ethical standards.



SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist.

Department Of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2025-26/02

CLASS CONSOLIDATED TIME TABLE

Class: V Semester

Section: A

Class Coordinator: Sri. M. PREMCHAND

w. e. f. 10-07-2025

Room No.: D-203

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	DC	DSUP	ADICA	DHDL	Lunch Break	Python Lab		
Tue	ADICA	ADICA Lab				AWP	DC	Sports
Wed	APT	ADC/ANT Lab				ADICA	DHDL	AWP
Thu	AWP	EE		DC		APT	DSUP	Sports
Fri	AWP	DC	DHDL	DSUP		DHDL	ADIC	LIB
Sat	DHDL	ADICA	DC	AWP		ANT/ADC Lab		

Section: B

Class Coordinator: Dr. B. ASHOK KUMAR

Room No.: D-202

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	AWP	DHDL	DC	LIB	Lunch Break	ADC/ANT Lab		
Tue	DHDL	AWP	DSUP	DC		ADICA	AWP	Sports
Wed	DSUP	DHDL	ADICA	AWP		DC	APT	
Thu	ADICA	ADICA Lab				DHDL	AWP	DC
Fri	DSUP	EE		ADICA		Python Lab		
Sat	DC	ANT/ADC Lab				DHDL	ADICA	Sports

Section: C

Class Coordinator: Sri. K. SAI SUDHEER

Room No.: A-204

Periods	1	2	3	4		5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		1:00-2:00	2:00-2:50	2:50-3:40
Mon	DHDL	ADC/ANT Lab			Lunch Break	DC	ADICA	DHDL
Tue	DC	ADICA	DHDL	LIB		Python Lab		
Wed	ADICA	EE		AWP		APT	DHDL	DC
Thu	AWP	DSUP	DHDL	APT		ADICA Lab		
Fri	DSUP	ANT/ADC Lab				ADICA	DC	AWP
Sat	DC	ADICA	AWP	DSUP		AWP	Sports	

Staff Details:

S. NO.	SUB	Course Name	SECTION		
			A	B	C
1.	ADICA	Analog & Digital IC Applications	Dr. B. ASHOK KUMAR	Dr. B. ASHOK KUMAR	Sri P.GOPALA REDDY
2.	DC	Digital Communications	Sri M. PREM CHAND	Sri M. PREM CHAND	Sri K.SAISUDHEER
3.	AWP	Antennas and Wave Propagation	Sri R. RAM PRASAD	Sri R. RAM PRASAD	Sri G.V.SUBRAHMANYAM
4.	DHDL	Digital System Design through HDL	Smt. Y.SUJATHA	Smt. Y.SUJATHA	Sri M. PITCHAIAH
5.	DSUP	Data Science Using Python	Dr. M. THAMARAI	Dr. M. THAMARAI	Dr. B. MURALI KRISHANA
6.	ADIC LAB	Analog & Digital IC Applications Lab	Dr. B. ASHOK KUMAR Dr.S.KAMESH Smt. Y.SUJATHA Dr. B. MURALI KRISHANA	Dr. B. ASHOK KUMAR Sri P.GOPALA REDDY Smt. Y.SUJATHA Dr. K. BABOJI	Dr. B. ASHOK KUMAR Dr. B. MURALI KRISHANA Sri.D.R.SANDEEP Sri P.GOPALA REDDY
7.	ADC LAB	Analog and digital communications Lab	Sri M. PREM CHAND Sri P.NAGARAJU	Dr. T.V.N.L. ASWINI Dr. K. BABOJI	Sri D.VENKANNA BABU Sri R.L.R.LOKESH BABU
8.	Antennas Lab	Antennas Lab	Sri G. SHANKARA BHASKARA RAO Smt.Q. AYN	Sri M. VINOD KUMAR Sri P.NAGARAJU	Sri R. RAM PRASAD Sri. A. SURESH
9.	DSUP Lab	Data Science Using Python Lab	Dr. M. THAMARAI Dr.S.V.V.SATYANARAYANA	Dr. M. THAMARAI Dr.E.KUSUMA KUMARI	Dr. B. MURALI KRISHANA Dr.E.KUSUMA KUMARI
10.	EE	English for Employability	Ms.A.KIRANMAYEE	SMT.CH.TANUJA	Dr.K.VENKATA RAO
11.	APT	Aptitude	Sri T.H.S.SRINIVAS	Sri J.N.V.SOMAYAJULU	Sri T.H.S.SRINIVAS

E. K. Suresh

Head of the Department

Vision: To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

Mission: To create a learner centric environment that promotes the intellectual growth of the students.

To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.

To build the student community with high ethical standards.



SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist.

Department Of Electronics and Communication Technology



Ref. No: SVEC/ECE/Circular/2025-26/02

CLASS CONSOLIDATED TIME TABLE

Class: V Semester

w. e. f. 10-07-2025

Section: ECT

Class Coordinator: Sri. M. PITCHAIAH

Room No.: A-203

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	DSUP	ADICA	DC	AWP	Lunch Break	DHDL	DSUP	Sports
Tue	DHDL	ADC/ANT Lab				ADICA	DC	AWP
Wed	DHDL	AWP	ADICA	DC		Python Lab		
Thu	ADICA	ANT/ADC Lab				DC	APT	
Fri	DC	ADICA	AWP	DHDL		EE		Sports
Sat	AWP	DSUP	DHDL	LIB		ADICA Lab		

Staff Details:

S. NO.	SUB	Course Name	SECTION
			ECT
1.	ADICA	Analog & Digital IC Applications	Sri P.GOPALA REDDY
2.	DC	Digital Communications	Sri K.SAISUDHEER
3.	AWP	Antennas and Wave Propagation	Sri G.V.SUBRAHMANYAM
4.	DHDL	Digital System Design through HDL	Sri M. PITCHAIAH
5.	DSUP	Data Science Using Python	Dr. B. MURALI KRISHANA
6.	ADIC LAB	Analog & Digital IC Applications Lab	Dr. B. MURALI KRISHANA Sri P.GOPALA REDDY Sri M. PITCHAIAH Sri D.VENKANNA BABU
7.	ADC LAB	Analog and digital communications Lab	Sri D.R.SANDEEP Sri K.SAISUDHEER
8.	Antennas Lab	Antennas Lab	Sri G.V.SUBRAHMANYAM Sri J. RAJENDRA
9.	DSUP Lab	Data Science Using Python Lab	Dr. B. MURALI KRISHANA Dr.S.V.V.SATYANARAYANA
10.	EE	English for Employability	MR.M.VENKATA RAMANA
11.	APT	Aptitude	Sri J.N.V.SOMAYAJULU

E. Kumaresh

Head of the Department

Vision:

To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

Mission:

- To create a learner centric environment that promotes the intellectual growth of the students.
- To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
- To build the student community with high ethical standards.



SRI VASAVI ENGINEERING COLLEGE (Autonomous)

Pedatadepalli,TADEPALLIGUDEM-534101,W.G.Dist.

Department of Electronics and Communication Engineering



Ref.No: SVEC/ECE/Circular/2025-26/03

CLASSCONSOLIDATEDTIMETABLE

Class: VII Semester

w.e.f.24-07-2025

Section: A

Class Coordinator: Sri. N. SAIDULU

Room No.: A-301

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	SA	ML	CMC	CDICD	Lunch Break	DIP	MSC	Sports
Tue	CDICD	Project				MSC	ML	DIP
Wed	ML	DIP	MSC	Lib		CDICD	SA	CMC
Thu	CMC	SA	ML	MSC		Project		
Fri	MSC	CMC	SA	CDICD		SA	DIP	Counseling
Sat	DIP	Project				CDICD	CMC	ML

Section: B

Class Coordinator: Dr. K. BABOJI

Room No.: A-303

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CDICD	CMC	DIP	ML	Lunch Break	Project		
Tue	DIP	CMC	CDICD	MSC		CDICD	MSC	LIB
Wed	MSC	Project				ML	OCN	DIP
Thu	ML	CMC	OCN	OCN		DIP	MSC	CDICD
Fri	CDICD	CMC	ML	MSC		Project		
Sat	OCN	CMC	DIP	Counseling		ML	OCN	Sports

Section: C

Class Coordinator: Sri. R. L. R. LOKESH BABU

Room No.: BSH-105

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CDICD	Project			Lunch Break	ML	MSC	Lib
Tue	DIP	MSC	CDICD	CMC		SA	ML	Counseling
Wed	CMC	DIP	MSC	SA		Project		
Thu	ML	CDICD	CMC	DIP		CMC	SA	CDICD
Fri	SA	Project				DIP	CMC	MSC
Sat	MSC	SA	ML	DIP		ML	CDICD	Sports

Staff Details:

S. No.	Course Code	Sub	Course Name	SECTION		
				A	B	C
1.	V20ECT20	DIP	Digital Image Processing	Sri N.SAIDULU	Sri N.SAIDULU	Sri T.SREENIVASU
2.	V20ECT22	CMC	Cellular& Mobile Communication	Sri M.VINOD KUMAR	Dr. E.KUSUMA KUMARI	Sri P.NAGARAJU
3.	V20ECT25	CDICD	COMS Digital IC Design	Dr. K. BABOJI	Dr. K. BABOJI	Dr. S.V.V. SATYANARAYANA
4.	V20ECTJOC14	ML	Machine Learning	Dr. T.V.N.L. ASWINI	Dr. T.V.N.L. ASWINI	Sri R.L.R LOKESH BABU
5.	V20ECTJOC02(Elective)	OCN	Optical Communication Networks	---	Dr. T.D.N.S.S. SARVESWARARAO	---
6.	V20ECTJOC04	MSC	Modern Satellite Communication	Dr. KNH SRINIVAS	Sri M. VINOD KUMAR	Sri M. VINOD KUMAR
7.	V20ECTJOC12(Elective)	SA	Sensors & Applications	Sri V. ANILKUMAR	---	Sri V. ANILKUMAR

E. K. K. K.

Head of the Department

Vision: Todevelopthedevelopmentintoacentreofexcellenceandproducehighquality,technicallycompetentandresponsibleElectronicsandCommunicationEngineers.

Mission:

- Tocreatealearnercentricenvironmentthatpromotestheintellectualgrowthofthestudents.
- TodeveloplinkageswithR&Dorganizationsandeducationalinstitutionsforexcellenceinteaching,learningandconsultancy practices.
- Tobuildthestudentcommunitywithhighethical standards.



SRI VASAVI ENGINEERING COLLEGE (Autonomous)

Pedatadepalli,TADEPALLIGUDEM-534101,W.G.Dist.

Department Of Electronics and Communication Technology



Ref.No: SVEC/ECE/Circular/2025-26/03

CLASSCONSOLIDATEDTIMETABLE

Class: VII Semester

Section: ECT

Class Coordinator: **Sri. P. NAGARAJU**

w.e.f.24-07-2025

Room No.: BSH-106

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CMC	OCN	SA	CDICD	Lunch Break	ML	DIP	Sports
Tue	OCN	CMC	SA	Counseling		Project		
Wed	ML	CDICD	CDICD	LIB		DIP	CMC	OCN
Thu	DIP	Project				CDICD	ML	DIP
Fri	CDICD	CMC	CMC	ML		SA	OCN	ML
Sat	SA	DIP	SA	OCN		Project		

StaffDetails:

S. No.	Course Code	Sub	Course Name	ECT
1.	V20ECT20	DIP	Digital Image Processing	Sri T.SREENIVASU
2.	V20ECT22	CMC	Cellular & Mobile Communication	Sri P.NAGARAJU
3.	V20ECT25	CDICD	COMS Digital IC Design	Dr. S.V.V. SATYANARAYANA
4.	V20ECTJOC14	ML	Machine Learning	Dr. S.KAMESH
5.	V20ECTJOC02(Elective)	OCN	Optical Communication Networks	Dr.CH. V .NAGA BHASKAR
6.	V20ECTJOC12	SA	Sensors& Applications	Dr.K.N.H.SRINIVAS

E. K. Srinivas

Head of the Department

Vision:

To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

Mission:

- To create a learner centric environment that promotes the intellectual growth of the students.
- To develop linkages with R&D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
- To build the student community with high ethical standards.